

<b>Notice of References Cited</b>	Application/Control No. 09/966,121	Applicant(s)/Patent Under Reexamination WALACAVAGE ET AL.	
	Examiner Kibrom K. Gebresilassie	Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Satoshi Kanai, and Takeshi Kishinami, herein referred as Kanai, "A Virtual Verification Environment for the Sequence Control System Using VRML and JAVA", 1999 by ASME, pgs 1-8.
	V	Matthew Rohrer, "AUTOMOD PRODUCT SUITE TUTORIAL (Automod, Simulator, AutoSat) BY AUTOSIMULATIONS" Proceeding of the 1999 Winter Simulation Conference, pgs. 220-226.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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